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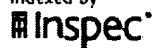
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## A hardware/software co-simulation environment for micro design with HDL simulator and OS interface

[Ito, Y.](#) [Nakamura, Y.](#)

C&C Res. Labs., NEC Corp., Kawasaki, Japan;

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### Abstract

Proposes a hardware/software co-simulation environment using an RTL (register transfer a software language interface. The proposed simulation environment introduces the "oper. interface" (OSIF), which invokes system calls in the OS on the simulation platform to exec software. The OSIF consists of data adaption facility and function correspondence management cooperate with the OS of the simulation platform. We show the results of experiments with compatible processor model. This environment verified our processor model with SPEC b require various OS services. For example, with the Lisp interpreter program li, our detailed core part of R3000 was simulated only within 20 hours on a 109 MIPS workstation

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Inspec

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